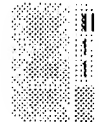


L Number	Hits	Search Text	DB	Time stamp
17	8	5469549.URPN. and (control\$4 with (bits variables parameters))	USPAT	2004/11/08 13:37
18	0	5469549.URPN. and (pair\$1 with (bits variables parameters))	USPAT	2004/11/08 13:37
19	2	5469549.URPN. and (pair\$1 with (bit\$1 variable\$1 parameter\$1))	USPAT	2004/11/08 13:38
-	222	butler with (circuit\$1 chip\$1)	USPAT; US-PGPUB; DERWENT	2004/11/05 16:12
-	233	butler with (circuit\$1 chip\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/11/05 11:35
-	13	(butler with (circuit\$1 chip\$1) same (integrated near circuit\$1))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/11/05 11:37
-	4	(butler with (circuit\$1 chip\$1) same (integrated near circuit\$1)) and tile\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/11/05 11:44
-	0	(butler with (circuit\$1 chip\$1) same (integrated near circuit\$1)) and stim-wait	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/11/05 13:21
-	4	5168566.URPN. and (integrated) and logic\$2	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/11/05 13:26
-	6	3643227.URPN. and (integrated near circuit\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/11/05 14:42
-	20	(campbell near eric).in.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/11/05 14:42
-	6	(hugo near simpson).in.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/11/05 15:05
-	1	(pairs near5 (control\$4 adj variables)) and (integrated near circuit\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/11/05 15:16
-	0	(pairs near5 (control\$4 adj variables)) and (schedul\$3 near3 task\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/11/05 15:17
-	0	(pairs near5 (control\$4 adj variables)) and (processor\$1 near10 task\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/11/05 15:17
-	56	(pair\$1 near5 variables) and (processor\$1 near10 task\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/11/05 15:18
-	18	(pair\$1 near5 variables) and (schedul\$4 near5 task\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/11/05 15:18
-	640	((two-dimension\$2 near2 array) and circuit\$1) and schedul\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/11/05 16:08
-	0	((two-dimension\$2 near2 array) and circuit\$1) same (schedul\$4 near task\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/11/05 16:08

-	6	((two-dimension\$2 near2 array) and circuit\$1) same schedul\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/11/05 16:14
-	22	(butler with matrix\$2) and schedul\$4	USPAT; US-PGPUB; DERWENT	2004/11/05 16:27
-	11	((two-dimension\$2 near2 array) with circuit\$1) and (schedul\$4 near10 task\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/11/05 16:14

IEEE Xplore  
RELEASE 1.2Welcome  
United States Patent and Trademark Office

» See:

[Help](#) [FAQ](#) [Terms](#) [IEEE Peer Review](#)[Quick Links](#)

## Welcome to IEEE Xplore

- ☐ Home
- ☐ What Can I Access?
- ☐ Log-out

## Index of Contents

- ☐ Journals & Magazines
- ☐ Conference Proceedings
- ☐ Standards

## Search

- ☐ By Author
- ☐ Basic
- ☐ Advanced
- ☐ CrossRef

## Member Services

- ☐ Join IEEE
- ☐ Establish IEEE Web Account
- ☐ Access the IEEE Member Digital Library

## IEEE Enterprise

- ☐ Access the IEEE Enterprise File Cabinet



Print Format

Your search matched **1** of **1088345** documents.A maximum of **500** results are displayed, **15** to a page, sorted by **Relevance Descending** order.

## Refine This Search:

You may refine your search by editing the current search expression or entering a new one in the text box.

☐ Check to search within this result set

## Results Key:

**JNL** = Journal or Magazine    **CNF** = Conference    **STD** = Standard1 **An 8-bit multitask micropower RISC core***Perotto, J.-F.; Lamothe, C.; Arm, C.; Piguet, C.; Dijkstra, E.; Fink, S.; Sanche Wattenhofer, J.-P.; Cecchini, M.;*Solid-State Circuits, IEEE Journal of , Volume: 29 , Issue: 8 , Aug. 1994  
Pages:986 - 991[\[Abstract\]](#)    [\[PDF Full-Text \(472 KB\)\]](#)    **IEEE JNL**

[Home](#) | [Log-out](#) | [Journals](#) | [Conference Proceedings](#) | [Standards](#) | [Search by Author](#) | [Basic Search](#) | [Advanced Search](#) | [Join IEEE](#) | [Web Account](#) | [New this week](#) | [OPAC Linking Information](#) | [Your Feedback](#) | [Technical Support](#) | [Email Alerting](#) | [No Robots Please](#) | [Release Notes](#) | [IEEE Online Publications](#) | [Help](#) | [FAQ](#) | [Terms](#) | [Back to Top](#)

[options](#)[logout](#)[feedback](#)[help](#)[databases](#)[easy search](#)

## Advanced Search: INSPEC - 1969 to date (INZZ)

[limit](#)

Search history:

No.	Database	Search term	Info added since	Results	
1	INZZ	butler NEXT chip	unrestricted	0	-
2	INZZ	butler NEAR chip	unrestricted	0	-
3	INZZ	butler NEAR circuit	unrestricted	11	<a href="#">show titles</a>
4	INZZ	butler NEAR circuit AND tasks	unrestricted	0	-
5	INZZ	butler NEAR matrix	unrestricted	149	<a href="#">show titles</a>
6	INZZ	butler NEAR matrix AND integrated NEXT circuit	unrestricted	2	<a href="#">show titles</a>
7	INZZ	butler NEAR matrix AND scheduler	unrestricted	0	-

[hide](#) | [delete all search steps...](#) | [delete individual search steps...](#)Enter your search term(s): [Search tips](#)Information added since: or: 

(YYYYMMDD)

[search](#)

Select special search terms from the following list(s):

- ☒ Classification codes A: Physics, 0-1
- ☒ Classification codes A: Physics, 2-3
- ☒ Classification codes A: Physics, 4-5
- ☒ Classification codes A: Physics, 6
- ☒ Classification codes A: Physics, 7
- ☒ Classification codes A: Physics, 8
- ☒ Classification codes A: Physics, 9
- ☒ Classification codes B: Electrical & Electronics, 0-5
- ☒ Classification codes B: Electrical & Electronics, 6-9
- ☒ Classification codes C: Computer & Control
- ☒ Classification codes D: Information Technology
- ☒ Classification codes E: Manufacturing & Production
- ☒ Treatment codes
- ☒ INSPEC sub-file
- ☒ Publication types
- ☒ Language of publication